Substitute for form 1449A/PTO	Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Application Number	10/594,799	
	Filing Date	September 28, 2006	
	First Named Inventor	Zach Yoav	
	Group Art Unit	2196	
	Examiner Name	Tang, Kenneth	
	Attorney Docket No	42390P23148	
Sheet 1 of 1			

	US PATENT DOCUMENTS						
Examiner Initial *	Cite No ¹	USP Document Number	Publication or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
/K1/		2003/0126313 A1	7/3/2003	Kerly			
/KT/		2005/0071841 A1	3/31/2005	Hoflehner et al.			

	01	THER DOCUMENTS - NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
/KT/	1	PCT Notification of Transmittal of the International Search Report and the Written Opinion of the International Searching Authority, or the Declaration for PCT Counterpart Application No. PCT/CN2006/000580, 9 pages, (January 11, 2007).	
	2	"Instruction Set Architecture (ISA)", http://kmh.ync.ac.kr/PcNcMicro/student2.html, 5 pages, (July 8, 2001).	
8	3	"Instruction Set Architecture (ISA)", http://shekel.jct.ac.il/~citron/ca/isa.html, 4 pages, (August 8, 2002).	1
00000	4	JOSEPH D. WIEBER, JR., et al., "Introduction to Intrinsics", http://www.intel.com/cd/ids/developer/asmo-na/eng/59644.htm?prn=Y, 3 pages, (January 26, 2006).	
00000	5	"Intel Extended Memory 64 Technology", http://www.intel.com/technology/64bitextensions/, 2 pages, (June 8, 2004).	
0000000	6	"Preparing Code for the IA-64 Architecture (Code Clean) – A Programmer's Reference", Intel Corporation, 36 pages, (2000).	
000000	7	CRISTINA CIFUENTES, et al., "Walkabout – A Retargetable Dynamic Binary Translation Framework", Sun Microsystems, Inc., 33 pages, (January 2002).	
	8	"Thread (computer science)", http://en.wikipedia.org/wiki/Multithreading, 8 pages, (March 21, 2002).	1
W	9	"Instruction Set", http://en.wikipedia.org/wiki/Instruction_set, 4 pages, (April 5, 2002).	†
/K Ť /	10	LEONID BARAZ, et al., "IA-32 Execution Layer: A two-phase dynamic translator designed to support IA-32 applications on Itanium-based systems", Intel Corporation, 11 pages, (September 25, 2003).	

EXAMINER /Kenneth Tang/ DATE CONSIDERED 03/28/2011